Design Description:

**Design Methodologies**: We use Top Down methodologies for our design. Our Top Level block is SPI Master . We define Control Unit, Input and Output as our sub block . After that , we try to follow our Methodology and divide our sub block to another sub block. In our deign sub-sub-block is FiFO, MOSI, MISO and Clock Divider.

**Module Name and Description:**

* **Top Module**: Spi Master Top : Spi Master communicate with slave . It send data to slave and receive Data from slave by choosing different address. Whole communication initiate and finish by this Module .
* **Sub Module** : Input, Output and control Unit are the sub module for Spi master Top. Input send data to Slave and Output collect data from slave . Control unit control whole process by managing Data Available and Read Request signal.
* **Sub Sub Module** : FIFO ,Mosi and Miso are the sub sub module . FIFO responsible for communicate with user and Miso , Mosi communicate with slave .